

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10050394	01/16/2002	257		2811	CKCU

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\*\*CONTINUING DATA VERIFIED:

Note - TD

\*\* FOREIGN APPLICATIONS VERIFIED:

Note - TD

PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND	
Foreign priority claimed	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		F0466
Verified and Acknowledged Examiner's initials	TD		
TITLE : Diode fabrication for ESD/EOS protection			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claims for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Fig.s Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL		Primary Examiner		Application Examiner
<input type="checkbox"/> DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		

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